

L Number	Hits	Search Text	DB	Time stamp
1	0	(device adj driver adj logic) same hdl	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 09:49
2	24	device adj driver adj logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 09:49
-	1960	programmable adj2 chip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:06
-	4	(programmable adj2 chip) same core same peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:34
-	19	(single adj2 chip) same core same peripheral same programmable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:39
-	6464	uart	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:40
-	248365	timer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:40
-	869492	interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:40
-	165	custom\$3 adj2 peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:46
-	4577	wizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 07:56
-	1	((single adj2 chip) same core same peripheral same programmable) and uart	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:42
-	8	((single adj2 chip) same core same peripheral same programmable) and timer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:42
-	16	((single adj2 chip) same core same peripheral same programmable) and interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:43
-	8	((single adj2 chip) same core same peripheral same programmable) and timer) and interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/09 09:44

-	0	((((single adj2 chip) same core same peripheral same programmable) and timer) and interface) and (custom\$3 adj2 peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:44
-	8	((((single adj2 chip) same core same peripheral same programmable) and timer) and interface) and ((single adj2 chip) same core same peripheral same programmable)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:45
-	0	(custom\$3 adj2 peripheral) and ((single adj2 chip) same core same peripheral same programmable)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:46
-	0	(custom\$3 adj2 peripheral) and (programmable adj2 chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:46
-	67	custom\$3 adj peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:46
-	0	uart and (custom\$3 adj peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:46
-	0	(singgle adj chip) and (custom\$3 adj peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:47
-	5	(single adj chip) and (custom\$3 adj peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/09 09:47
-	44302	(programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/16 10:08
-	834917	parameter\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/16 10:09
-	13940	parameter\$6 same core	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/16 10:10
-	254847	(peripheral or interface) and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/16 10:12
-	14670	((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and parameter\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/16 10:12
-	455	((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/16 10:13

-	347	((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:13
-	323	implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:13
-	0	configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:14
-	303	configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:14
-	158	synthes\$5 and (configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:15
-	158	description and (synthes\$5 and (configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:15
-	135	interconnect\$3 and (description and (synthes\$5 and (configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:16
-	477	custom\$6 adj4 peripheral	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:18
-	0	(interconnect\$3 and (description and (synthes\$5 and (configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core)))))) and (custom\$6 adj4 peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:17
-	0	(description and (synthes\$5 and (configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core)))))) and (custom\$6 adj4 peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:17
-	15	((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (custom\$6 adj4 peripheral)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:23
-	7	parameter\$5 and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (custom\$6 adj4 peripheral))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:25
-	4615	wizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:26

-	1	(parameter\$5 and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (custom\$6 adj4 peripheral))) and wizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:27
-	244	((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and wizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:27
-	11	(description and (synthes\$5 and (configur\$6 and (implement\$5 and ((peripheral or interface) and processor) and (((programmable adj2 chip) or (programmable adj2 device) or (programmable adj2 logic)) and (parameter\$6 same core)))))) and wizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/16 10:27
-	1	wizard same integrated adj2 design	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 07:58
-	358	wizard same parameter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 07:59
-	0	subwizard same spawn\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 07:59
-	4	subwizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 08:02
-	2	parameter and subwizard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/21 09:46